

## **In Re Claims**

1. (Currently Amended) A system, comprising:
  - a first write state machine;
  - a second write state machine;
  - a pulse generator operable to generate a first current draw waveform of current to the first write state machine and a second current draw waveform of current to the second write state machine, wherein the first current draw waveform is substantially similar to the second current draw waveform and include a large initial pulse of current followed by a plurality of smaller pulses of current; and
  - a delay circuit operable to inject a time delay between the first current draw waveform and the second current draw waveform, and wherein the time delay is less than a duration of the current draw waveform applied to the first write state machine and greater than a duration of the large initial pulse of the first current draw waveform.
2. (Cancelled)
3. (Cancelled)
4. (Currently Amended) The system of Claim ~~21~~, wherein the large initial pulse of current of the second current draw waveform which is applied to the second write state machine occurs during a delay between the large initial pulse of current applied to the first write state machine and the plurality of smaller pulses of current applied to the first write state machine.

5. (Currently Amended) The system of Claim 21, wherein the large initial pulse of current of the second current draw waveform which is applied to the second write state machine occurs during a delay between a first plurality of smaller pulses of current applied to the first write state machine and a second plurality of smaller pulses of current applied to the first write state machine.

6. (Currently Amended) The system of Claim 21, wherein the large initial pulse of current which is applied to the first write state machine has an amplitude substantially equal to the amplitude of the large initial pulse of current which is applied to the second write state machine.

7. (Currently Amended) The system of Claim 21, wherein the plurality of additional pulses have amplitudes that are less than or equal to half of the amplitude of the large initial pulse of current applied to the first write state machine.

8. (Currently Amended) A method, comprising:

applying a first current draw waveform of current to a first write state machine;

delaying a second current draw waveform of current by a predetermined amount

of time from a start of the first current draw waveform, wherein the first current draw waveform is substantially similar to the second current draw waveform and include a large initial pulse of current followed by a plurality of smaller pulses of current; and

applying the second pulse of current to a second write state machine, wherein the predetermined amount of time is less than a duration of the first current draw waveform and greater than a duration of the large initial pulse of the first current draw waveform.

9. (Cancelled)

10. (Currently Amended) The method of Claim 89, wherein the large initial pulse of current which is applied to the first write state machine has an amplitude substantially equal to the amplitude of the large initial pulse of current which is applied to the second write state machine.

11. (Cancelled)

12. (Currently Amended) The method of Claim 89, wherein the second current draw waveform of current which is applied to the second write state machine occurs during a delay between the large initial pulse of current applied to the first write state machine and the plurality of smaller pulses of current applied to the first write state machine.

13. (Currently Amended) The method of Claim 89, wherein the second current draw waveform of current which is applied to the second write state machine occurs during a delay between a first plurality of smaller pulses of current applied to the first write state machine and a second plurality of smaller pulses of current applied to the first write state machine.

14. (Currently Amended) The method of Claim 89, wherein the plurality of additional pulses have amplitudes that are less than or equal to half of the amplitude of the large initial pulse of current applied to the first write state machine.

15. (Currently Amended) A computer-readable medium having computer-executable instructions, comprising:

applying a first current draw waveform of current to a first write state machine;

delaying a second current draw waveform of current by a predetermined amount

of time from a start of the first current draw waveform, wherein the first current draw waveform is substantially similar to the second current draw waveform and include a large initial pulse of current followed by a plurality of smaller pulses of current; and

applying the second pulse of current to a second write state machine, wherein the predetermined amount of time is less than a duration of the first current draw waveform and greater than a duration of the large initial pulse of the first current draw waveform.

16. (Cancelled)

17. (Currently Amended) The computer-readable medium of Claim ~~46~~15, wherein the large initial pulse of current which is applied to the first write state machine has an amplitude substantially equal to the amplitude of the large initial pulse of current which is applied to the second write state machine.

18. (Cancelled)

19. (Currently Amended) The computer-readable medium of Claim ~~46~~15, wherein the second current draw waveform of current which is applied to the second write state machine occurs during a delay between the large initial pulse of current applied to the first write state machine and the plurality of smaller pulses of current applied to the first write state machine.

20. (Currently Amended) The computer-readable medium of Claim ~~46~~15, wherein the second current draw waveform of current which is applied to the second write state machine occurs during a delay between a first plurality of smaller pulses of current applied to the first write state machine and a second plurality of smaller pulses of current applied to the first write state machine.

21. (Currently Amended) The computer-readable medium of Claim ~~46~~15, wherein the plurality of additional pulses have amplitudes that are less than or equal to half of the amplitude of the large initial pulse of current applied to the first write state machine.